

FIG. 1 (PRIOR ART)

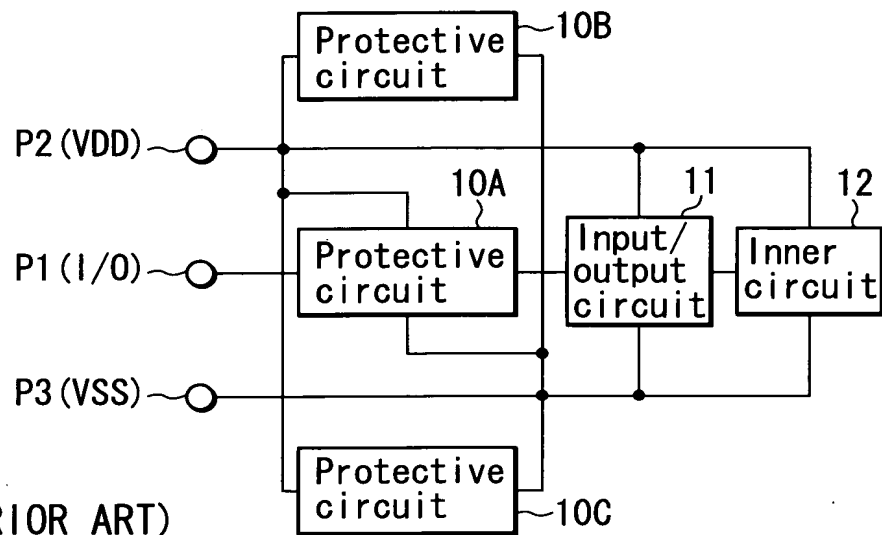


FIG. 2 (PRIOR ART)

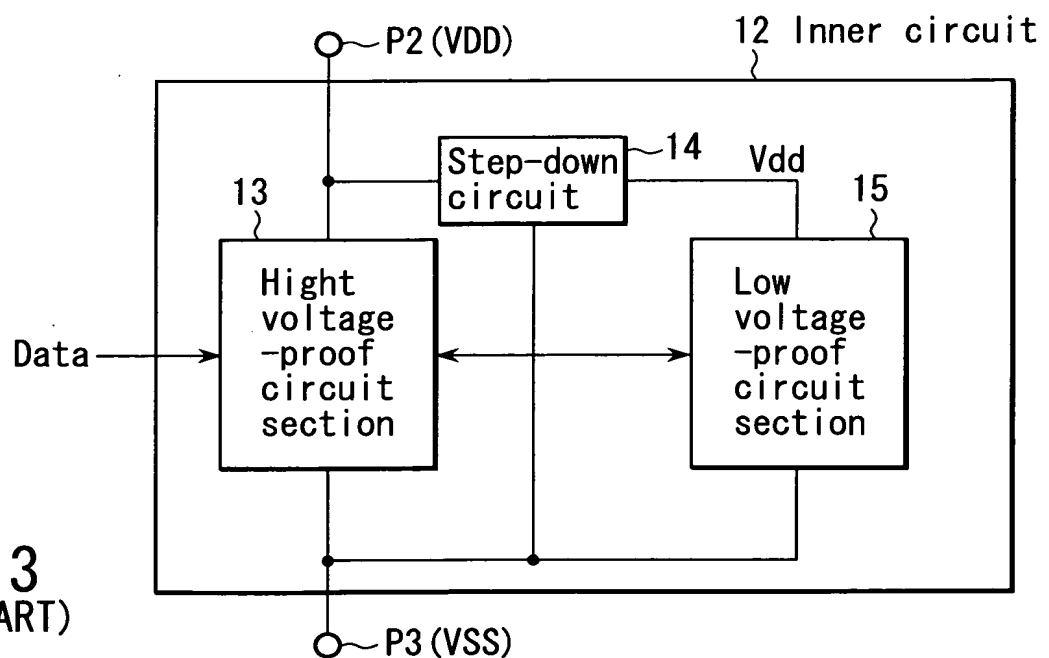


FIG. 3
(PRIOR ART)

Clamp characteristics of protective circuit

FIG. 4
(PRIOR ART)

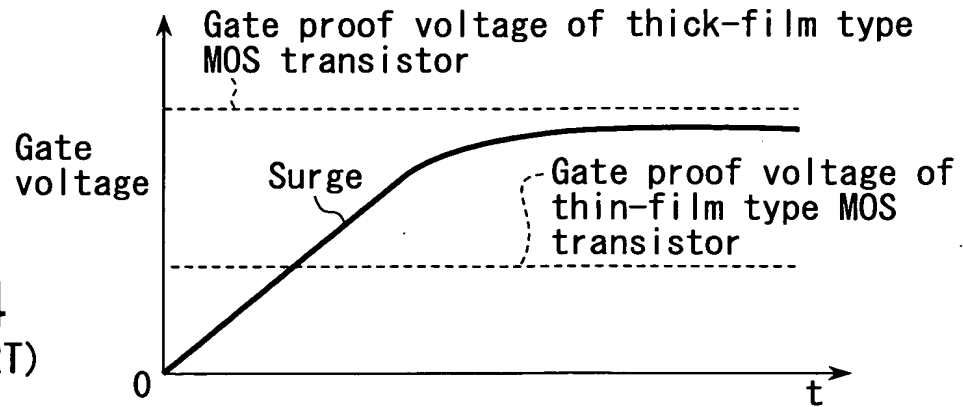


FIG. 8

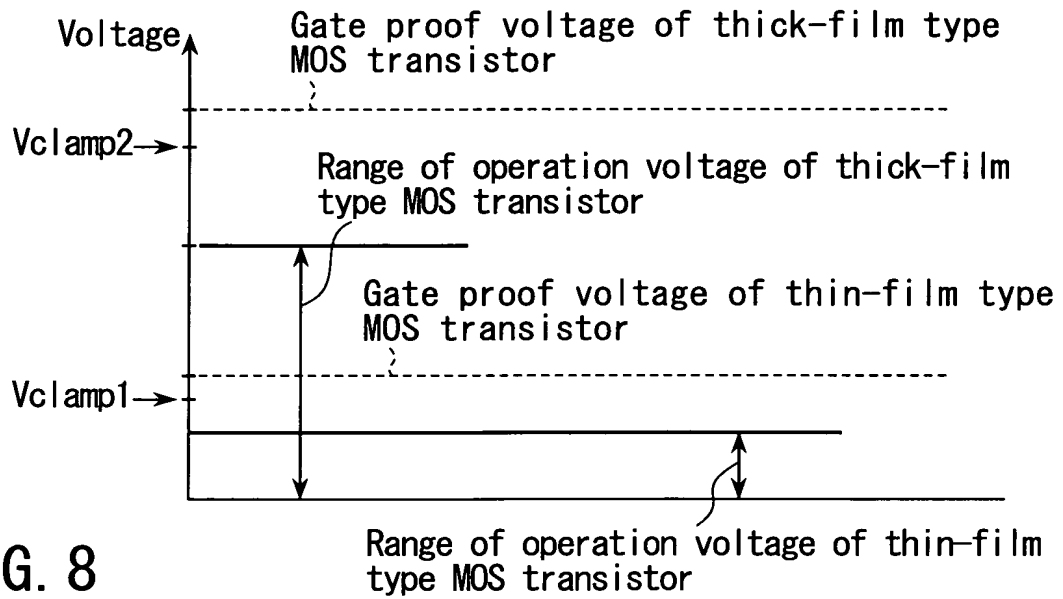
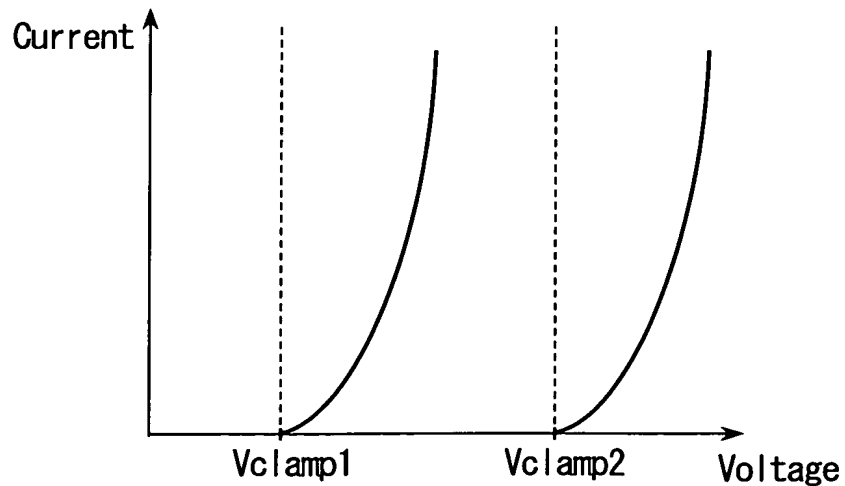


FIG. 9



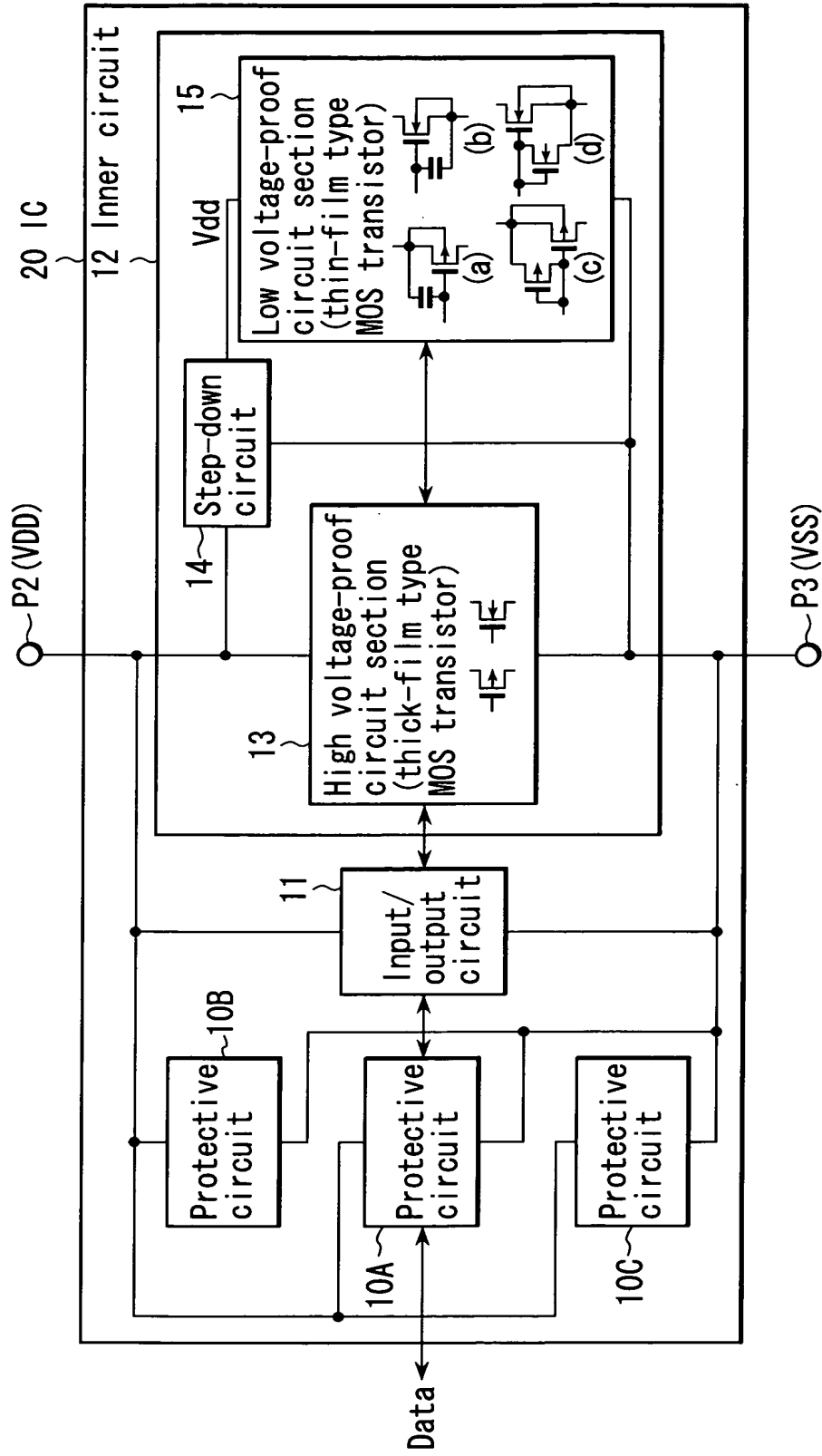


FIG. 5

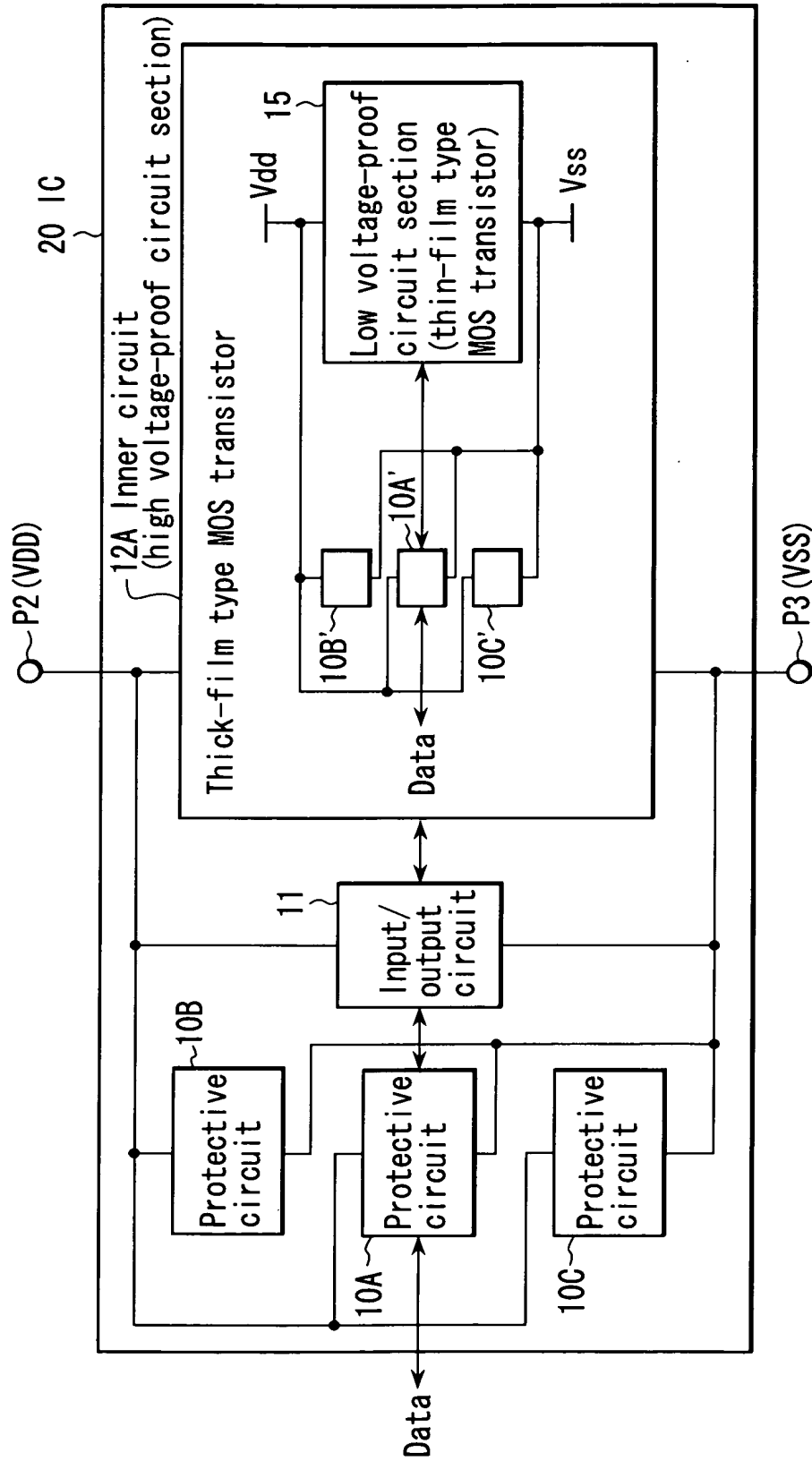


FIG. 6

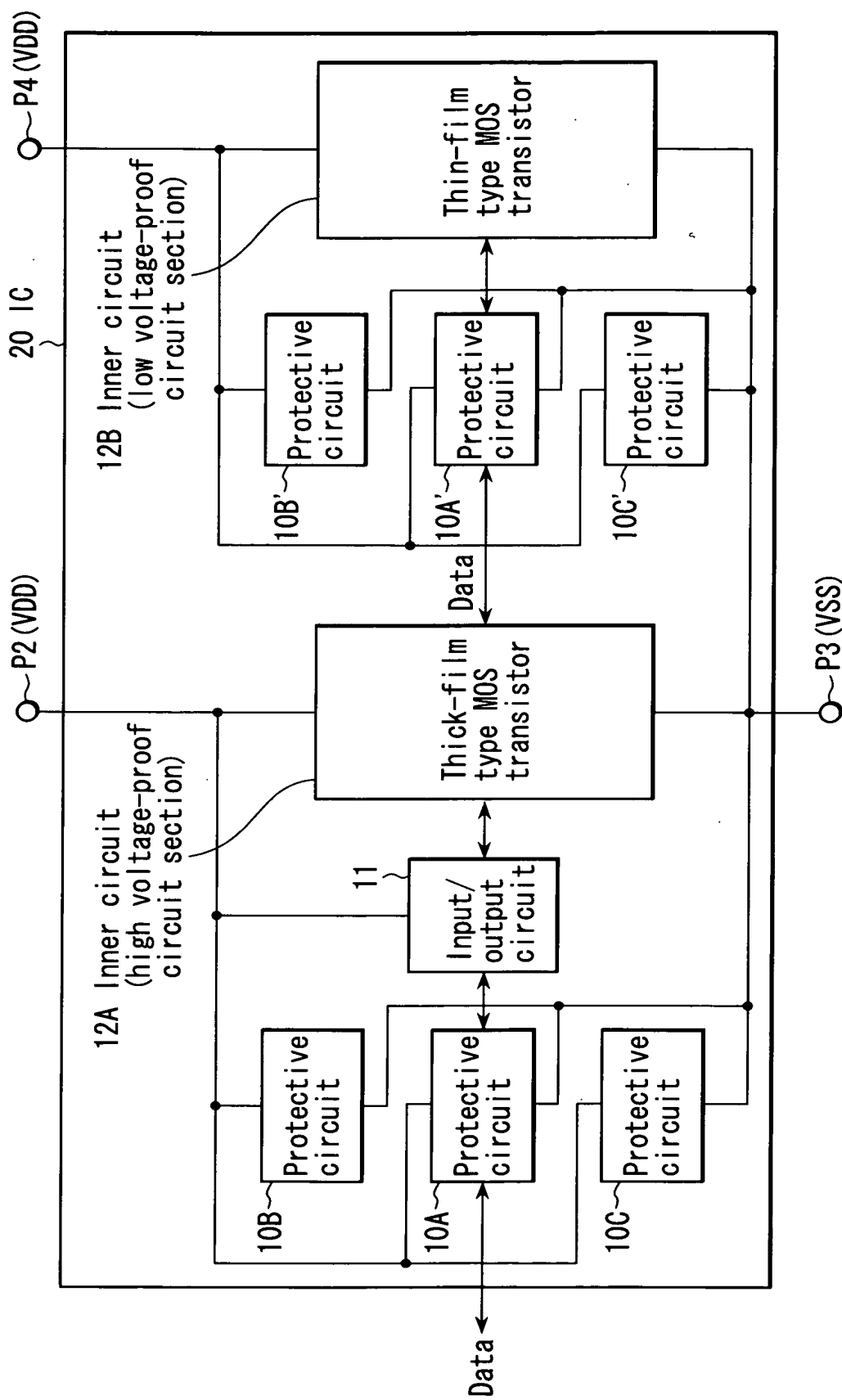


FIG. 7

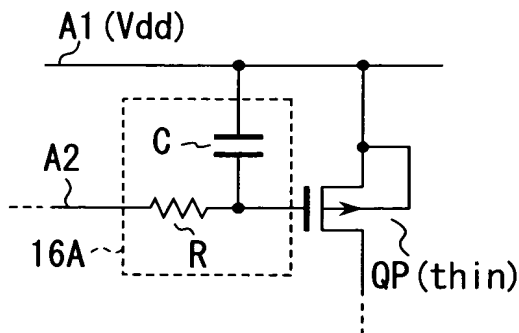
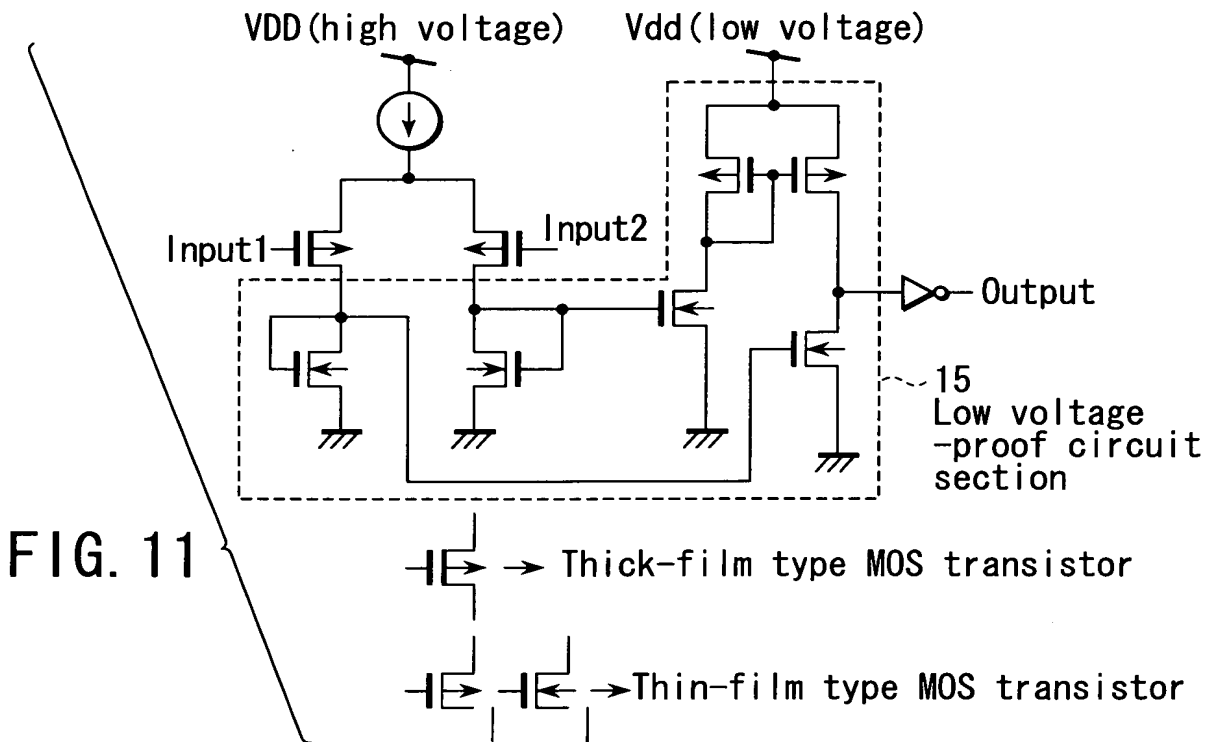
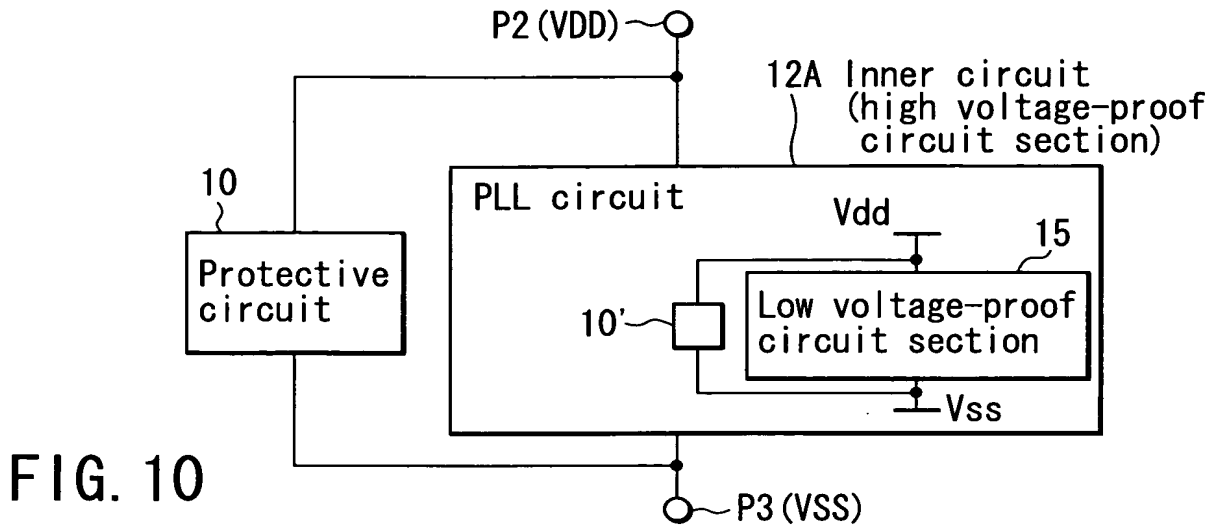


FIG. 12

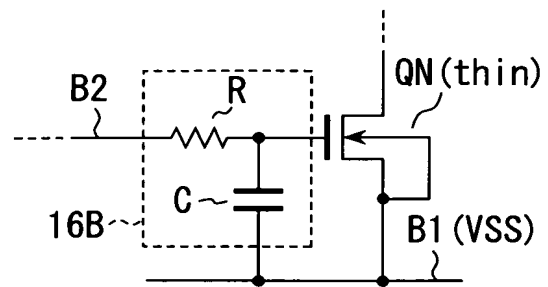


FIG. 13

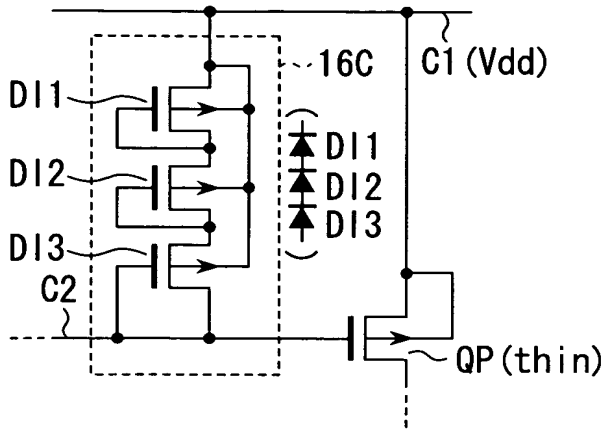


FIG. 14

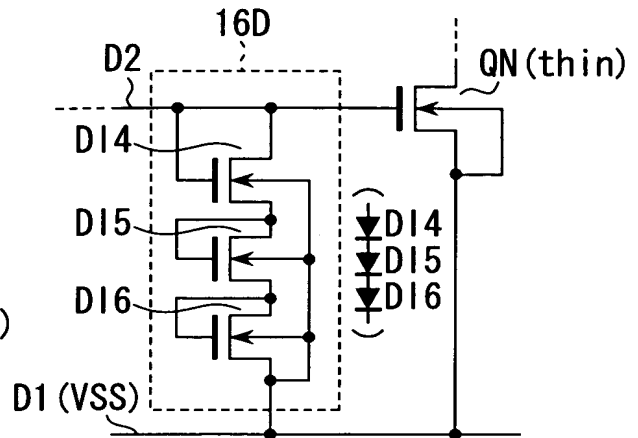


FIG. 15

Clamp characteristics of protective circuit
for thin-film type MOS transistor

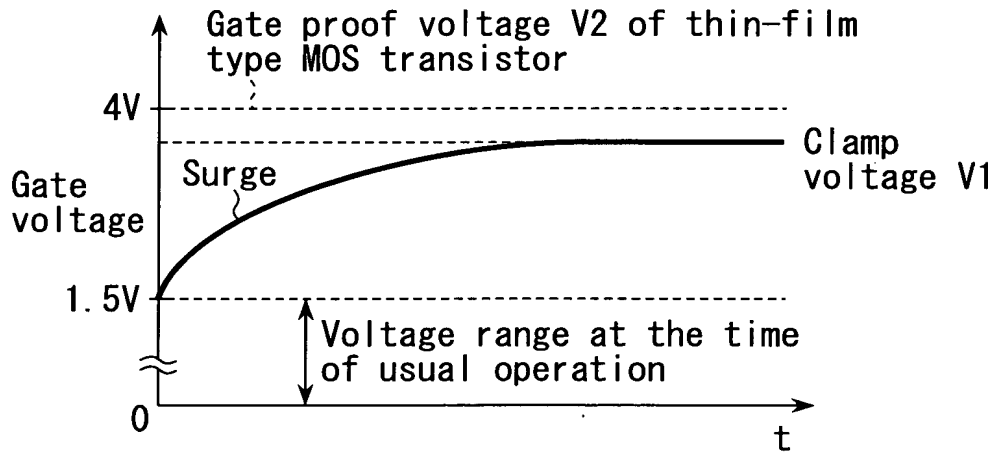


FIG. 16

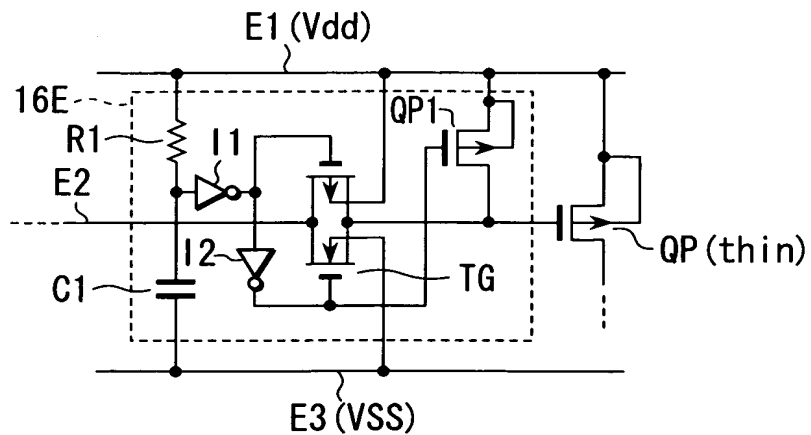


FIG. 17

